

## Refine Search

### Search Results -

Term	Documents
SASAKI-YASUSHI	84
SASAKI-YASUSHIS	0
SASAKI-YASUSHI.IN..PGPB,USPT.	84
(SASAKI-YASUSHI.IN.).PGPB,USPT.	84

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L3

Refine Search

Recall Text

Clear

Interrupt

### Search History

 DATE: Friday, September 23, 2005    [Printable Copy](#)    [Create Case](#)
SetName   Queryside by  
sideHit  
CountSet  
Name  
result set

DB=PGPB,USPT; PLUR=YES; OP=ADJ

L3   sasaki-yasushi.IN.84   L3L2   asaski-yasushi.IN.0   L2

DB=PGPB; PLUR=YES; OP=ADJ

L1   (pumping adj circuit and flat adj panel adj display and external adj control  
and substrate and transistor\$1 and capacitor\$1).CLM.2   L1

END OF SEARCH HISTORY

Interference Search  
Author

## Freeform Search

<b>Database:</b>	US Pre-Grant Publication Full-Text Database
	<b>US Patents Full-Text Database</b>
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins
<b>Term:</b>	122 and @py<=2001
<b>Display:</b>	20 Documents in Display Format: TI Starting with Number 1
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

### Search History

DATE: Friday, September 23, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L38</u>	122 and @py<=2001	35	<u>L38</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L37</u>	130 and (LCD or liquid adj crystal adj (display or device))	1	<u>L37</u>
<u>L36</u>	L35 and @py<=2001	4	<u>L36</u>
<u>L35</u>	(LCD or liquid adj crystal adj display) and pumping adj circuit	44	<u>L35</u>
<u>L34</u>	(LCD or liquid adj crystal adj display) same pumping adj circuit	6	<u>L34</u>
<u>L33</u>	flat adj panel adj display same pumping adj circuit same substrate	4	<u>L33</u>
<u>L32</u>	L31 and first adj capacitor and second capacitor	1	<u>L32</u>
<u>L31</u>	L30 and clock adj input and capacitor	4	<u>L31</u>
<u>L30</u>	L4 and pumping adj circuit and control adj circuit and substrate	99	<u>L30</u>
<u>L29</u>	L4 and pumping adj circuit and external adj control adj circuit	4	<u>L29</u>
<u>L28</u>	L4 and pumping adj circuit same external adj control adj circuit	1	<u>L28</u>
<u>L27</u>	L25 and scan\$4 driver\$1	1	<u>L27</u>
<u>L26</u>	L25 and scan\$4 driver\$1	1	<u>L26</u>
<u>L25</u>	L22 and substrate	25	<u>L25</u>

<u>L24</u>	L4 and pumping adj circuit and external adj control and scan\$4 adj driver\$1	1	<u>L24</u>
<u>L23</u>	L4 and pumping adj circuit and external adj control same scan\$4 adj driver\$1	1	<u>L23</u>
<u>L22</u>	L4 and pumping adj circuit and external adj control	42	<u>L22</u>
<u>L21</u>	L4 and first adj transistor same second adj transistor same third adj transistor	17	<u>L21</u>
<u>L20</u>	L4 and transistor adj pairs same opposite adj polarities	0	<u>L20</u>
<u>L19</u>	L4 and transistor adj pairs same opposite adj polaritie\$1	0	<u>L19</u>
<u>L18</u>	L4 and first adj transistor and second transistor same opposite adj polaritie\$1	0	<u>L18</u>
<u>L17</u>	L4 and first adj transistor same second transistor same opposite adj polaritie\$1	0	<u>L17</u>
<u>L16</u>	L4 and first adj transistor same second transistor same opposite adj polaritie\$1	0	<u>L16</u>
<u>L15</u>	L13 and output adj signal	13	<u>L15</u>
<u>L14</u>	L13 and output adj signal	13	<u>L14</u>
<u>L13</u>	L12 and capacitor\$1	20	<u>L13</u>
<u>L12</u>	L6 and clock same input	26	<u>L12</u>
<u>L11</u>	L9 and clock adj input same capacitor	1	<u>L11</u>
<u>L10</u>	L9 and clock adj input	6	<u>L10</u>
<u>L9</u>	L6 and capacitor	48	<u>L9</u>
<u>L8</u>	L6 and (first or second) adj capacator	0	<u>L8</u>
<u>L7</u>	L6 and external adj control adj circuit	1	<u>L7</u>
<u>L6</u>	L4 and pumping adj circuit same control adj circuit	87	<u>L6</u>
<u>L5</u>	L4 and pumping adj circuit same external adj control adj circuit	1	<u>L5</u>
<u>L4</u>	L2 and @py<=2001	1215	<u>L4</u>
<u>L3</u>	L2 @py<=2001	0	<u>L3</u>
<u>L2</u>	pumping adj circuit\$1	1703	<u>L2</u>
<u>L1</u>	punping adj circuit\$1	0	<u>L1</u>

END OF SEARCH HISTORY